

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("6236231").PN.	USPAT	OR	OFF	2006/11/09 11:07
L2	37	(display control) and (scal\$3 adj engine) and (tim\$3 and interface) with mode	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:10
L3	30	2 and (@ad<="20030926")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:15
L4	6	(display control\$3) and mode and scal\$3 with (tim\$3 adj control\$3) and (interface adj signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:19
L5	1359	display and (tim\$3 adj controller) and select\$3 and (interface circuit) with mode	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:51
L6	3	5 and (scal\$3 adj engine)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:23
L7	3	(display adj control) with (interfac\$3 adj circuit) with (phase-locked loop PLL)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:43

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L8	597	5 and scal\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:29
L9	44	8 and (TTL LVDS)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:30
L10	39	9 and (@ad<="20030926")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:37
L11	94	5 and 345/87,98,100,204.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:38
L12	65	11 and (@ad<="20030926")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:43
L13	2	(display adj control) with (interfac\$3 adj circuit) with (PLL LVDS) and scal\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:45
L14	2	display and (timing adj control) with scal\$3 and (interface adj circuit) with mode	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:48

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L15	42	display and (tim\$3 adj controller) and select\$3 and (interface circuit) with (mode adj control)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:51
L16	33	15 and (@ad<="20030926")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:56
L17	0	(controller adj mode) with scal\$3 with (first second third) adj (interface adj signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:58
L18	0	(controller adj mode) with scal\$3 with (interface adj signal) and (TTL LVDS)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:59
L19	0	(controller adj mode) with scal\$3 with (interface adj signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 11:59
L20	0	(controller adj mode) with scal\$3 with (interface adj circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 12:00
L21	53909	(display adj controller) and scal\$3 (interface adj circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 12:01

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L22	379	21 and 345/87,98,100,204.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 12:02
L23	288	22 and (@ad<="20030926")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/09 12:03